receiving an information transmission containing <u>processor instructions and</u> a program;

<u>programming said receiver station to perform a predetermined secondary error correction routine in accordance with said processor instructions;</u>

performing a primary error correction routine by processing at least [some] <u>a portion</u> of said information transmission;

passing information contained in said program to said memory;

discerning a failure evidencing <u>one of</u> an incomplete [or] <u>and an</u> incorrect program element in said memory by reprocessing information received in said information transmission; and

executing [a] <u>said</u> predetermined secondary error correction routine in consequence of said step of discerning a failure<u>;</u>

<u>wherein said method controls said receiver station.</u>

C₁ contd.-

6.   (Amended)   The method of claim 5, wherein one of said primary error correction routine and said secondary error correction routine comprises <u>the step of:</u>

clearing <u>at least a portion</u> [some or all] of said memory.

7.   (Amended)   The method of claim 5, further comprising the step of<u>:</u>

<u>one of</u> placing [or] <u>and</u> replacing data at said memory to <u>one of</u> complete [or] <u>and</u> correct a program element in consequence of said step of executing a predetermined secondary error correction routine.

8.   (Amended)   The method of claim 5, further comprising the step of<u>:</u>

interrupting a processor in accordance with one of said primary error correction routine and said secondary error correction routine.

9.   The method of claim 5, further comprising the steps of:

selecting a value designating an instruction to be executed; and

jumping to a memory location based on said selected value.

10.   The method of claim 5, wherein said step of selecting a value comprises computing at least some an address of said memory location.

11.   (Amended)   The method of claim 5, further comprising the steps of:

storing history-of-efficiency information; and

performing one of the functions of instituting [or] and restoring functionality of said at least one [of said one or more processors] processor based on said stored history of efficiency information.

12.   The method of claim 5, wherein said step of discerning a failure comprises comparing information stored at a first memory location to information stored at a second memory location.

13.   (Amended)   The method of claim 5, wherein at least one of said first memory location and said second memory location is a dedicated register at said at least one [or more processors] processor.

14.   The method of claim 5, wherein said primary error correction routine includes forward error correction and said step of discerning a failure is based on information processed in said step of performing primary error correction.

15.   (Amended)   The method of claim 5, wherein said one of said incomplete [or] and said incorrect program element in said memory is one of (1) one of an

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incomplete [or] and an incorrect element of said received program, and (2) [some or all] at least a portion of a second program.

16. (Amended) The method of claim 5, further comprising the step of: performing forward error correction information to be one of outputted in [or] and outputted with said program before performing said steps of (1) performing a primary error correction routine and (2) discerning a failure.

17. (Amended) The method of claim 5, wherein said step of performing a primary error correction routine further comprises:
selecting program material to be one of outputted and not [or not to be] outputted at said receiver station.

18. (Amended) The method of claim 5, further comprising selecting program material to be one of outputted and not [or not to be] outputted at said receiver station in accordance with said second error correction routine.

19. (Amended) The method of claim 5, wherein said program includes at least one [or more] of a television program, a radio program, a computer program, and some of a combined medium program.

20. (Amended) The method of claim 19, further comprising the step of: selecting at least one [or more] of a program instruction set, intermediate generation set, combining synchronizing command, and data to be processed to present combined medium programming.

21. (Amended) The method of claim 20, further comprising the step of:

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programming said receiver station with at least a portion [some] of said primary

error correction routine and said secondary error correction routine.

22. (Amended) The method of claim 21, wherein said step of programming said receiver station comprises:

receiving said at least a portion [some] of said primary error correction routine and said secondary error correction routine from a remote station;

directing said received at least a portion [some] of said primary error correction routine and said secondary error correction routine from [a] said remote station to at least one of a register and a re-programmable memory operatively connected to said at least one [or more processors] processor; and

storing said at least some of said primary error correction routine and said secondary error correction routine at said at least one of a register and a re-programmable memory operatively connected to said at least one [or more processors] processor.

23. (Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one [or more processors] processor operatively connected to said memory, comprising the steps of:

[(1)] receiving an information transmission at a transmission station, said information transmission containing only a portion of processor instruction and a program;

[(2)] generating the remainder of said [at least a portion of] processor instructions and [a] said program; and

[(3)] transmitting said information transmission containing said program and said processor instructions, wherein said processor instructions [to enable] program

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said receiver station to perform <u>a predetermined secondary error correction routine in accordance with said processor instructions, wherein said program enables said receiver station to perform</u> a primary error correction routine by processing at least <u>a portion</u> [some] of said information transmission, discerning a failure evidencing <u>one of</u> an incomplete [or] <u>and an</u> incorrect program element by reprocessing information received in said information transmission, and executing a predetermined secondary error correction routine in consequence of discerning a failure.

24.   (Amended)   A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and <u>at least</u> one [or more processors] <u>processor</u> operatively connected to said memory, comprising the steps of:

[(1)]   receiving an information transmission to be transmitted;

[(2)]   receiving an instruct signal which [is effective to] <u>one of</u>:

(a)   [effect] <u>effects</u> a transmission station to generate a program, said receiver station to perform a primary error correction routine by processing at least <u>a portion</u> [some] of said information transmission, discerning a failure evidencing <u>one of</u> an incomplete [or] <u>and an</u> incorrect program element by reprocessing information received in said information transmission, and executing a predetermined secondary error correction routine in consequence of discerning a failure; [or] <u>and</u>

(b)   [effect] <u>effects</u> a receiver station to generate a program, said receiver station to perform a primary error correction routine by processing at least <u>a portion</u> [some] of said information transmission, discerning a failure evidencing <u>one of</u> an incomplete [or] <u>and an</u> incorrect program element by reprocessing information received in said information transmission, and executing a predetermined secondary error correction routine in consequence of discerning a failure;

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[(3)]   receiving a transmitter control signal which operates at said transmitter station to communicate said program to a transmitter; and

[(4)]   transmitting said information transmission, said instruct signal and said transmitter control signal.--

Please add the following new claims:

25.   (New claim) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission containing mass medium programming including audio programming;

performing a primary error correction routine by processing at least a portion of said information transmission;

passing information contained in said mass medium programming to said memory;

discerning a failure evidencing one of an incomplete and an incorrect mass medium programming element in said memory by reprocessing information received in said information transmission; and

executing a predetermined secondary error correction routine in consequence of said step of discerning a failure;

wherein said method controls said receiver station.

26.   (New Claim) The method of controlling a receiver station of claim 25, wherein said step of executing a predetermined secondary error correction routine further includes the step of:

at least one of completing, correcting and discarding at least a portion of said

mass medium programming including said audio programming.

27.   (New Claim) A method of controlling a receiver station, said receiver

station including a receiver, a memory operatively connected to said receiver, and at

least one processor operatively connected to said memory, said method comprising the

steps of:

receiving an information transmission containing computer programming which

programs said receiver station;

performing a primary error correction routine by processing at least a portion of

said computer programming;

passing information contained in said computer programming to said memory;

discerning a failure evidencing one of an incomplete and an incorrect program

element in said memory by reprocessing said computer programming received in said

information transmission; and

executing a predetermined secondary error correction routine in accordance with

said received computer programming;

wherein said method controls said receiver station.

28.   (New Claim) A method of controlling a receiver station, said receiver

station including a receiver, a memory operatively connected to said receiver, and at

least one processor operatively connected to said memory, said method comprising the

steps of:

receiving an information transmission containing a program;

performing a primary error correction routine by processing at least a portion of

said information transmission;

passing information contained in said program to said memory;

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discerning a failure evidencing one of an incomplete and an incorrect program element in said memory by reprocessing information received in said information transmission;

selecting one of a plurality of predetermined secondary error correction routines to execute in consequence of said step of discerning a failure; and

executing said selected one of said plurality of predetermined secondary error correction routines;

wherein said method controls said receiver station.

29.   (New Claim) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission containing a program;

performing a primary error correction routine by processing at least a portion of said information transmission;

passing information contained in said program to said memory;

discerning a failure evidencing an incompletion of a function; and

executing a predetermined secondary error correction routine in consequence of said step of discerning a failure;

wherein said method controls said receiver station.

30.   (New Claim) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

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receiving an information transmission containing processor instructions and a program;

programming said receiver station to perform at least one error correction routine in accordance with said processor instructions;

performing a primary error correction routine by processing at least a portion of said information transmission;

passing information contained in said program to said memory;

discerning a failure evidencing one of an incomplete and an incorrect program element in said memory by reprocessing information received in said information transmission; and

executing a secondary error correction routine in consequence of said step of discerning a failure;

wherein at least one of a said primary error correction routine and said secondary error correction routine is performed in accordance with said processor instructions and wherein said method controls said receiver station.

31.   (New Claim) The method of controlling a receiver station of claim 30, wherein said program is mass medium programming.

32.   (New Claim) The method of controlling a receiver station of claim 30, wherein said program is computer programming.

33.   (New Claim) The method of controlling a receiver station of claim 30, wherein the step of discerning a failure further comprises the step of:

reprocessing information received in said information transmission.

34. (New Claim) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving at least one information transmission containing processor instructions and a program;

programming said receiver station to perform at least one error correction routine in accordance with said processor instructions;

performing at least one primary error correction routine by processing at least one portion of said at least one information transmission;

passing information contained in said program to said memory;

discerning a failure evidencing one of an incomplete and an incorrect program element in said memory; and

executing a secondary error correction routine in consequence of said step of discerning a failure;

wherein at least one of a said primary error correction routine and said secondary error correction routine is performed in accordance with said processor instructions and wherein said method controls said receiver station.

II. REMARKS

A. Introduction

The Office Action dated March 24, 1997 (Office Action) has been carefully reviewed and the foregoing amendments made in response thereto.

Claims 5-8, 11, 13, 15-24 are amended, and claims 25-34 are newly submitted. Claims 5-34 are pending in the application.